

FIG. 1
(PRIOR ART)

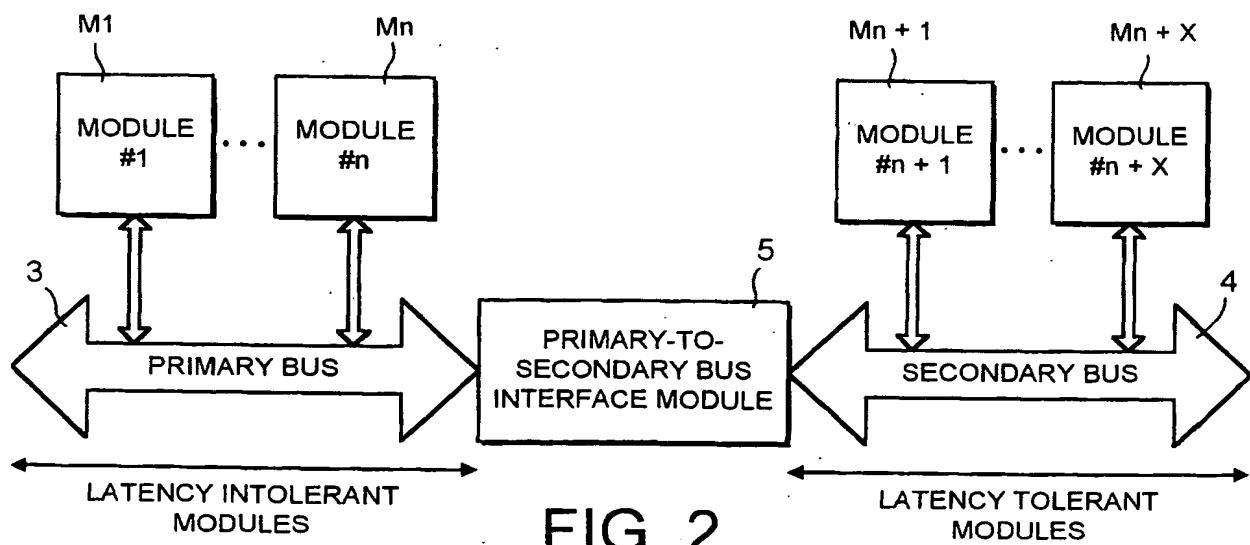


FIG. 2

Application Title: Apparatus For Use In A Computer System

Application Number: Unassigned (Div'l of 09/787,353)

Application Filing Date: April 20, 2004

Inventor: Richard Carl PHELPS et al.

Sheet 2 of 16

2 / 16

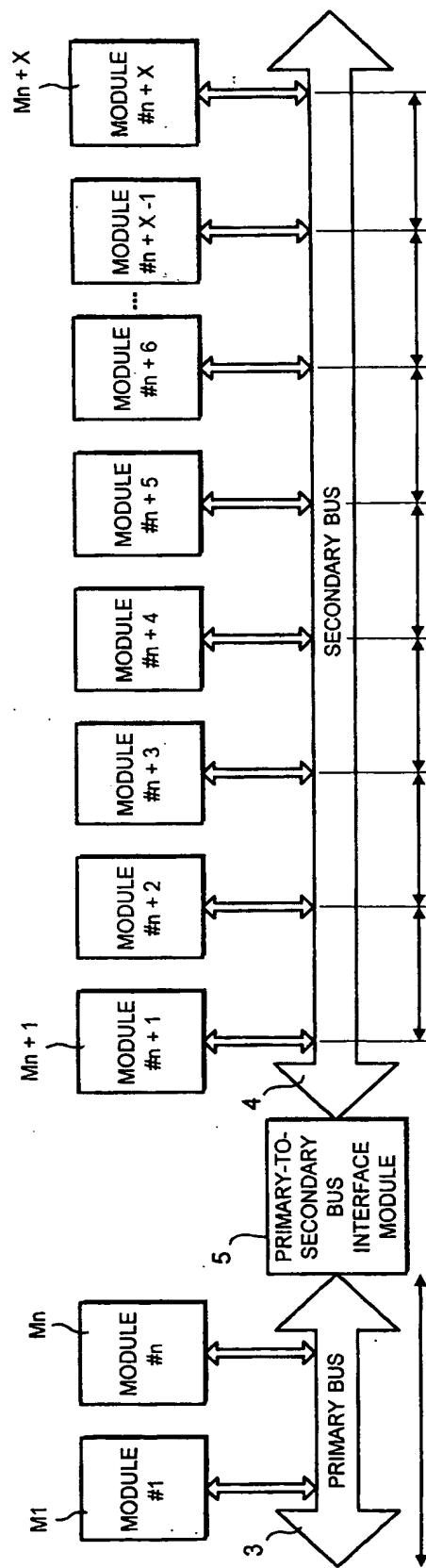


FIG. 3

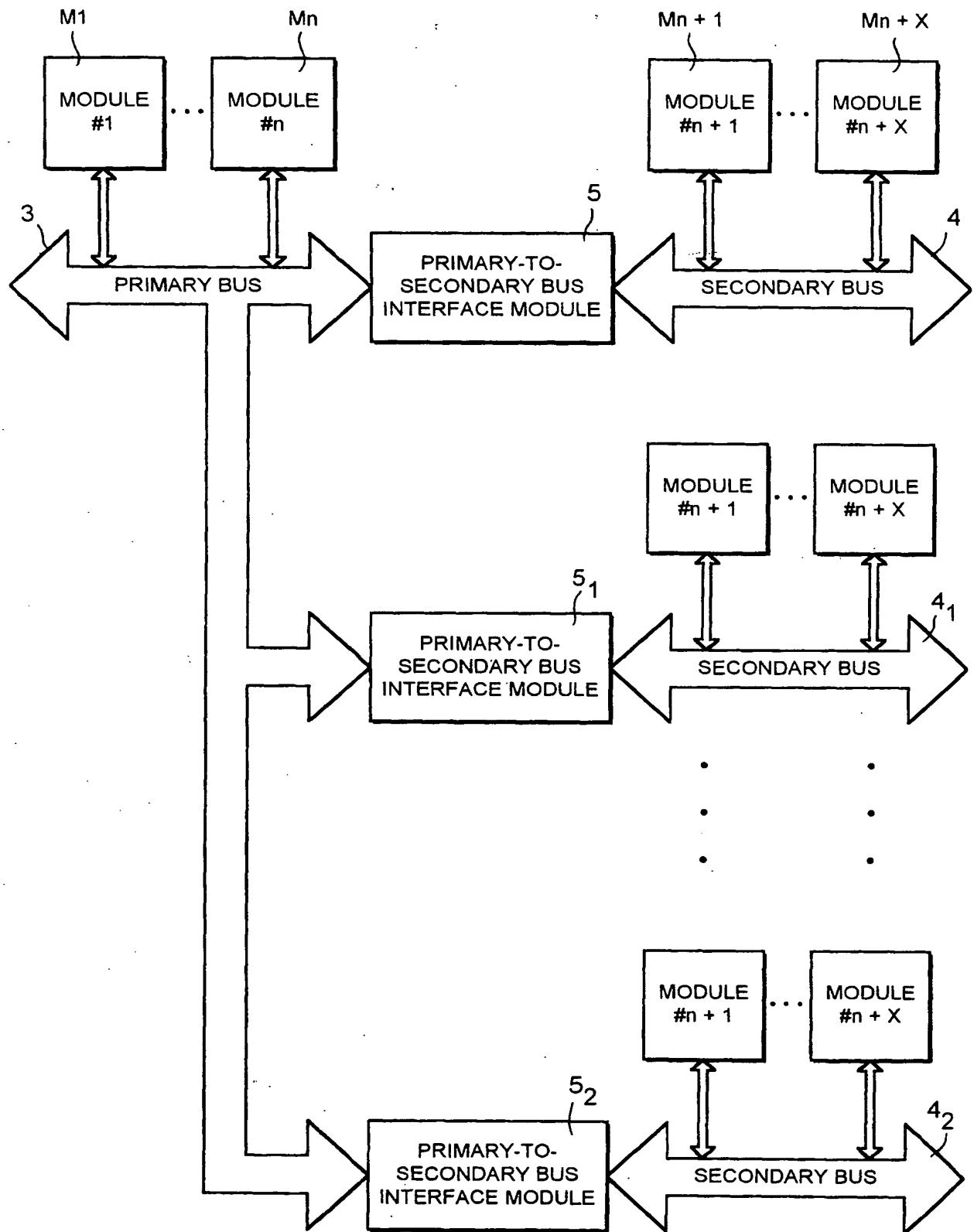


FIG. 4

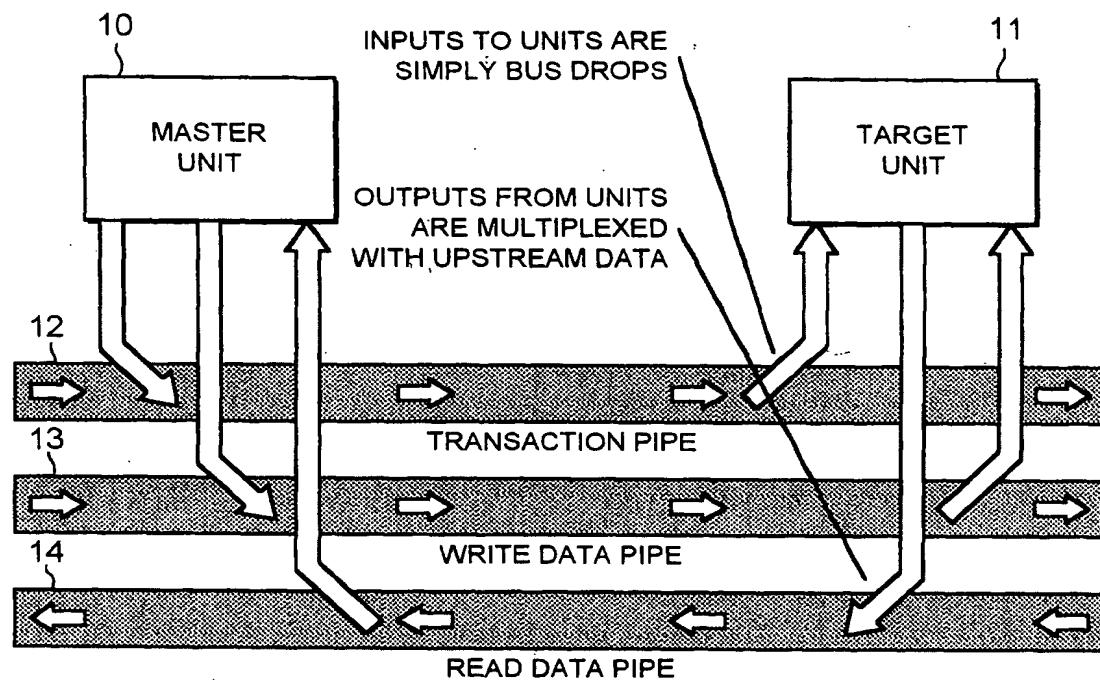


FIG. 5

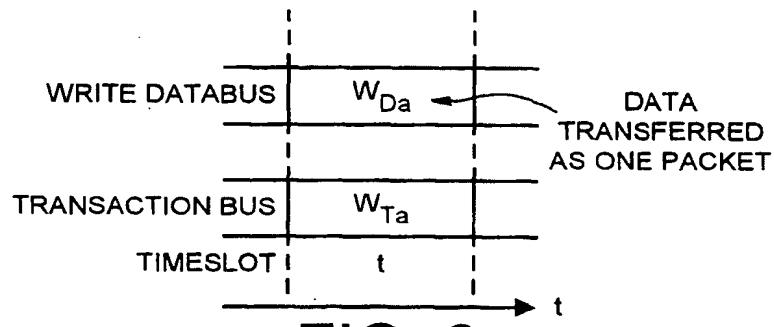


FIG. 6

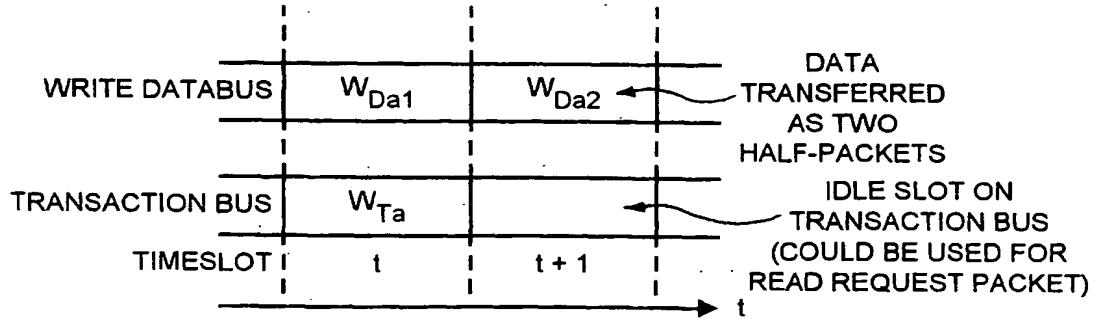


FIG. 7

Application Title: Apparatus For Use In A Computer System

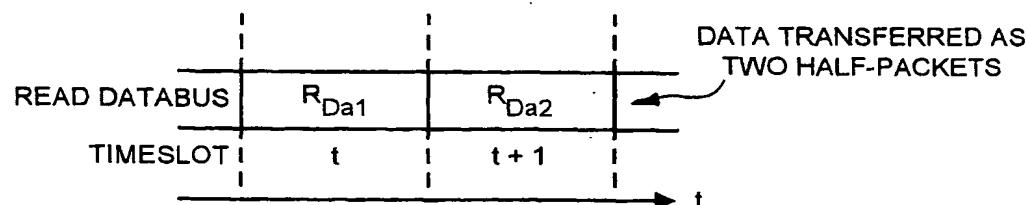
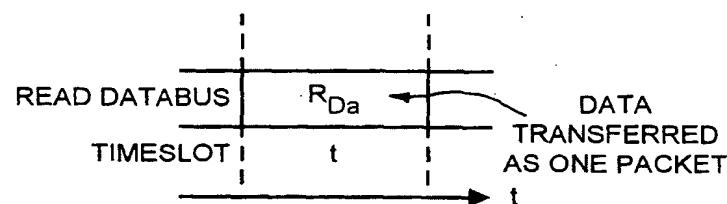
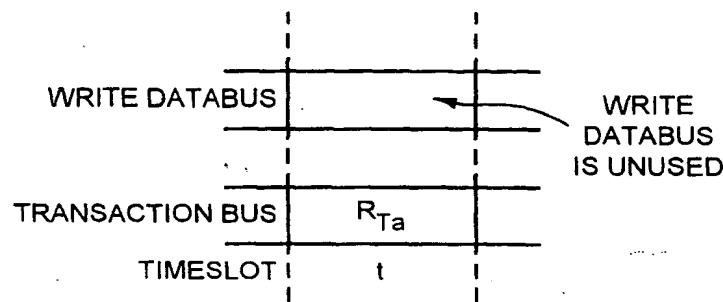
Application Number: Unassigned (Div'l of 09/787,353)

Application Filing Date: April 20, 2004

Inventor: Richard Carl PHELPS et al.

Sheet 5 of 16

5 / 16



Application Title: Apparatus For Use In A Computer System
Application Number: Unassigned (Div'1 of 09/787,353)
Application Filing Date: April 20, 2004
Inventor: Richard Carl PHELPS et al.

Sheet 6 of 16

6 / 16

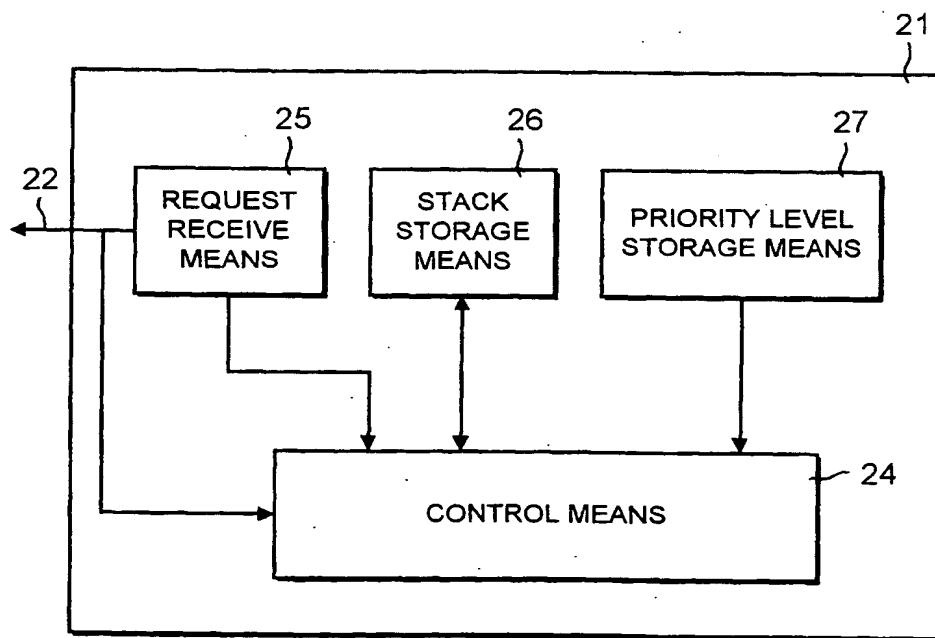
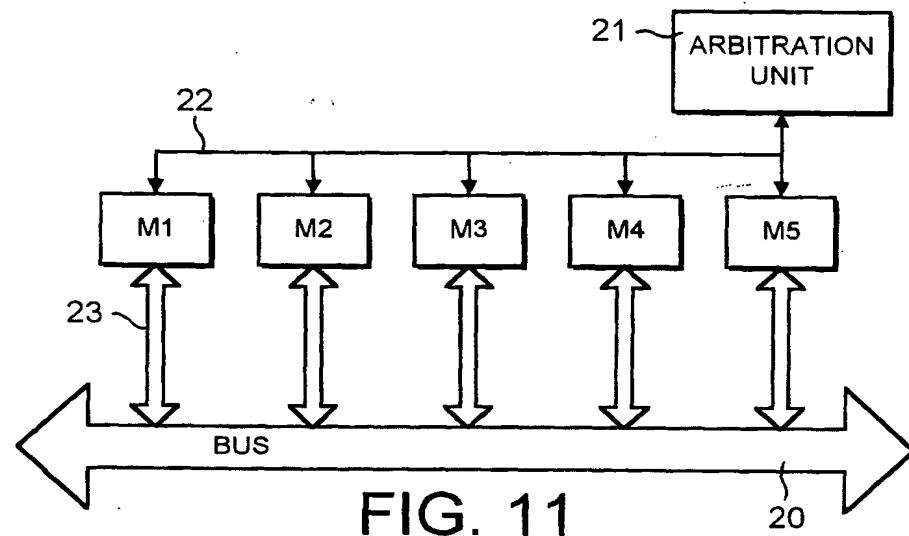


FIG. 12

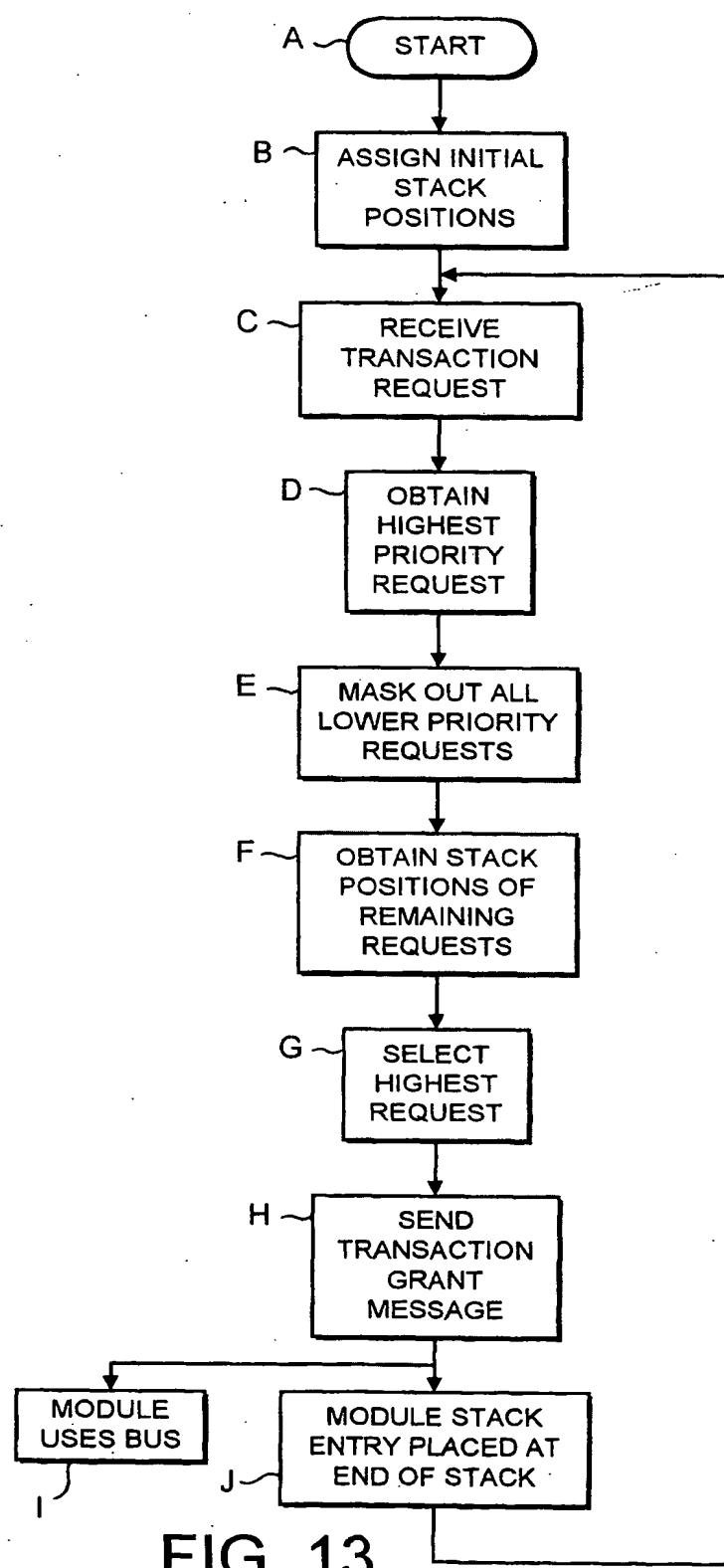


FIG. 13

Application Title: Apparatus For Use In A Computer System
Application Number: Unassigned (Div'l of 09/787,353)
Application Filing Date: April 20, 2004
Inventor: Richard Carl PHELPS et al.

Sheet 8 of 16
8 / 16

MODULE	PRIORITY
M1	H
M2	M
M3	L
M4	H
M5	M

FIG. 14

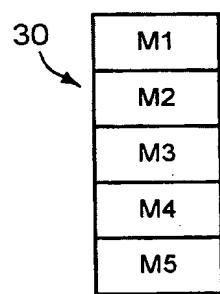


FIG. 15

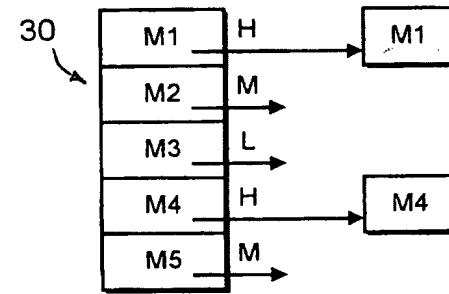


FIG. 16

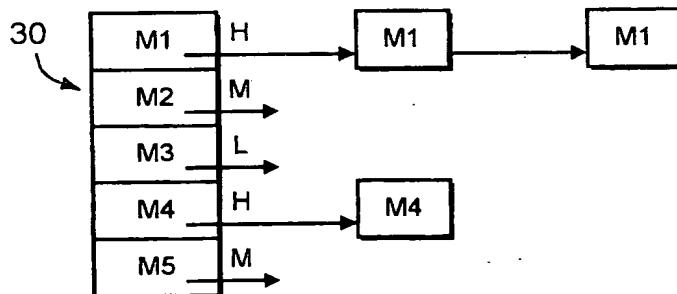


FIG. 17

Application Title: Apparatus For Use In A Computer System
Application Number: Unassigned (Div'l of 09/787,353)
Application Filing Date: April 20, 2004
Inventor: Richard Carl PHELPS et al.

Sheet 9 of 16

9 / 16

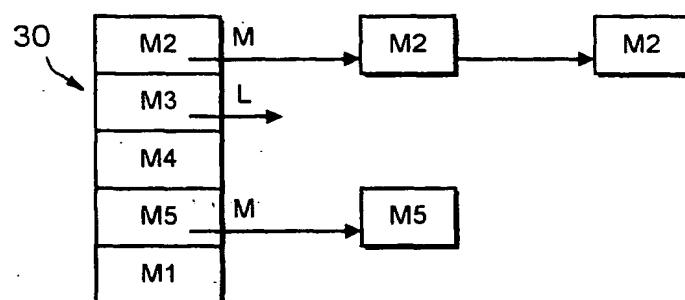


FIG. 18

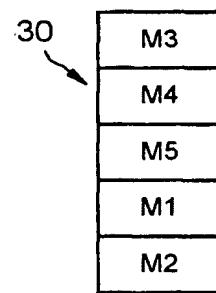


FIG. 19

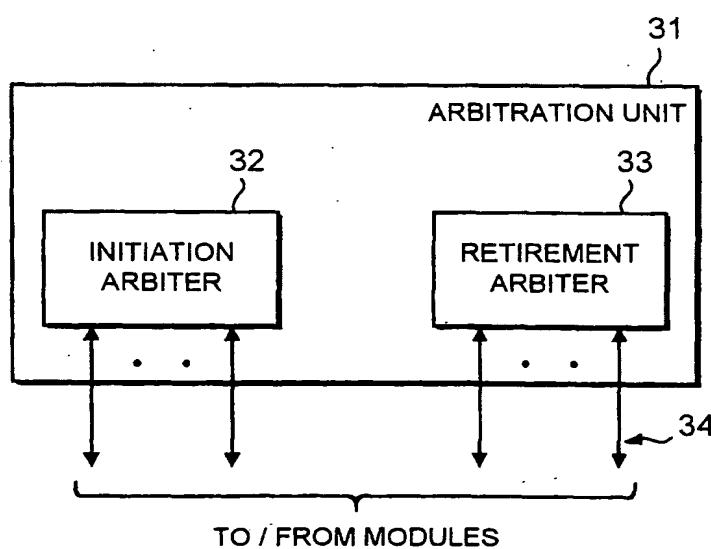


FIG. 20

Application Title: Apparatus For Use In A Computer System
Application Number: Unassigned (Div'l of 09/787,353)
Application Filing Date: April 20, 2004
Inventor: Richard Carl PHELPS et al.

Sheet 10 of 16

10 / 16

35 — T	R1	W1	R2	W2	R3	W3
36 — W		W1A	W1B	W2A	W2B	W3A	W3B
37 — R				R1A	R1B		
TIME	t	t	t	t	t	t	t

FIG. 21

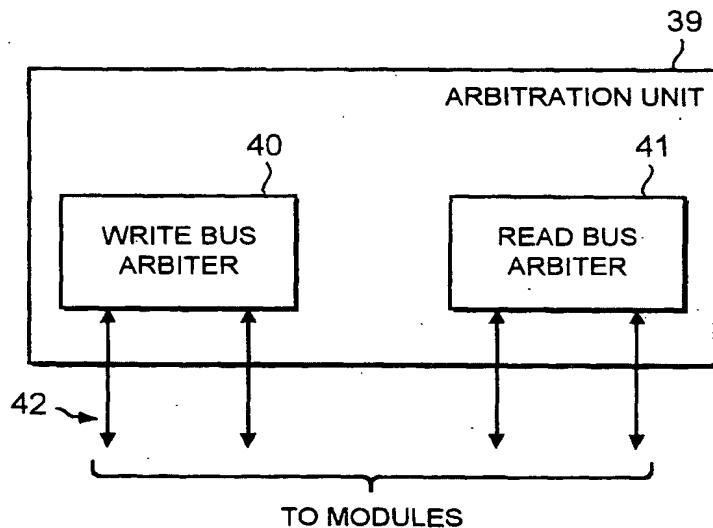


FIG. 22

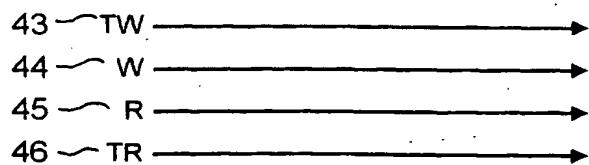


FIG. 23

Application Title: Apparatus For Use In A Computer System

Application Number: Unassigned (Div'l of 09/787,353)

Application Filing Date: April 20, 2004

Inventor: Richard Carl PHELPS et al.

Sheet 11 of 16

11 / 16

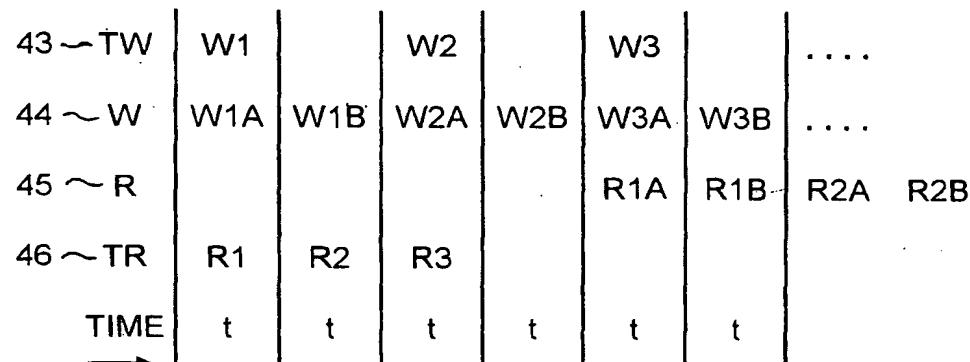


FIG. 24

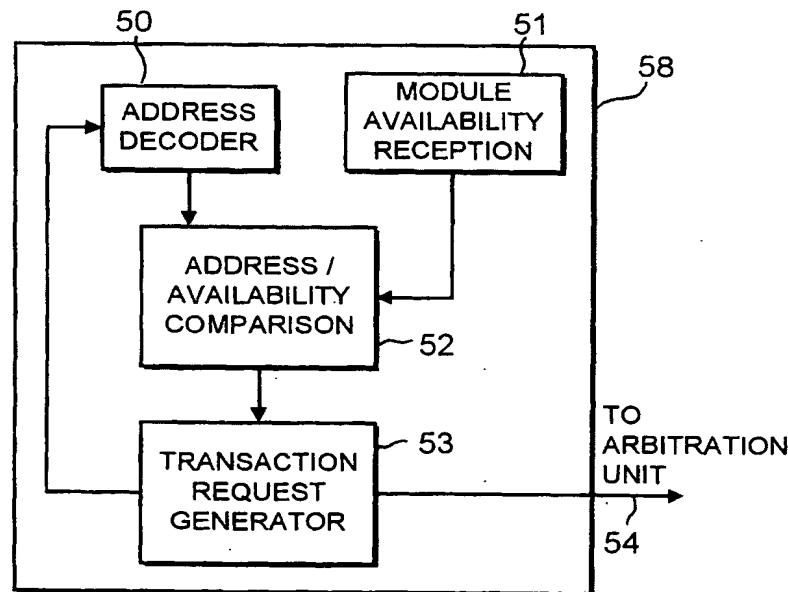


FIG. 25

Application Title: Apparatus For Use In A Computer System
Application Number: Unassigned (Div'l of 09/787,353)
Application Filing Date: April 20, 2004
Inventor: Richard Carl PHELPS et al.

Sheet 12 of 16

12 / 16

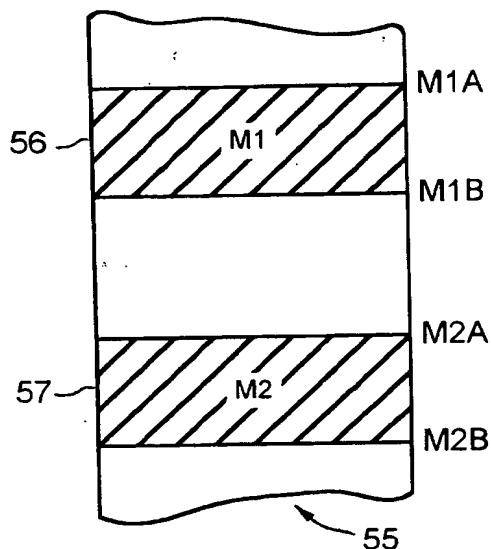


FIG. 26

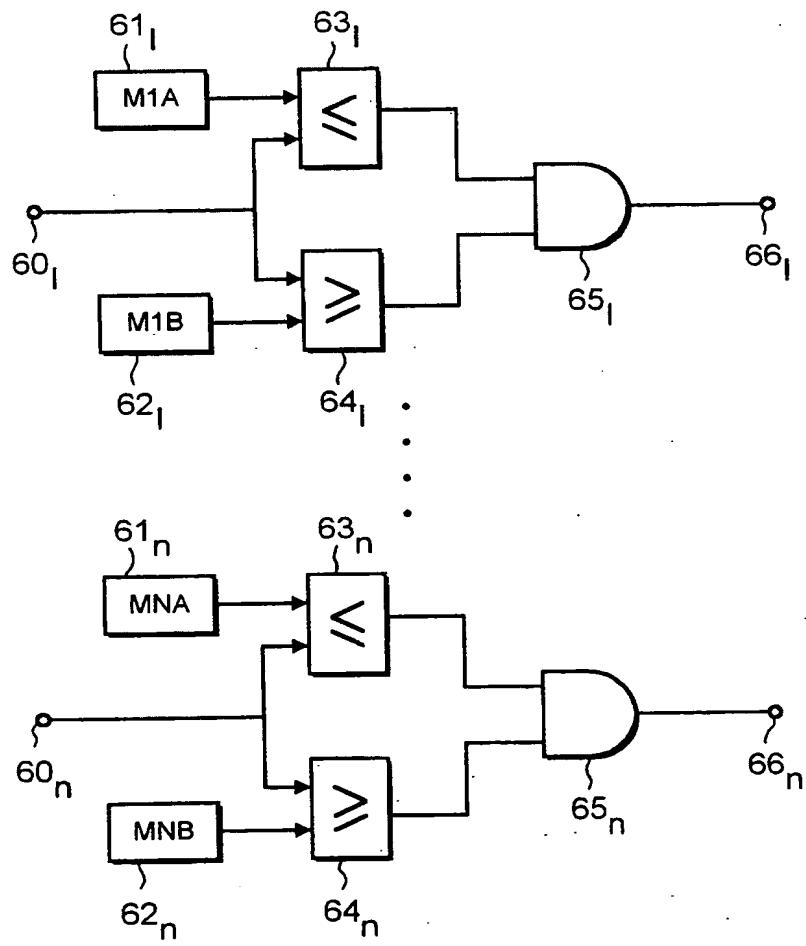


FIG. 27

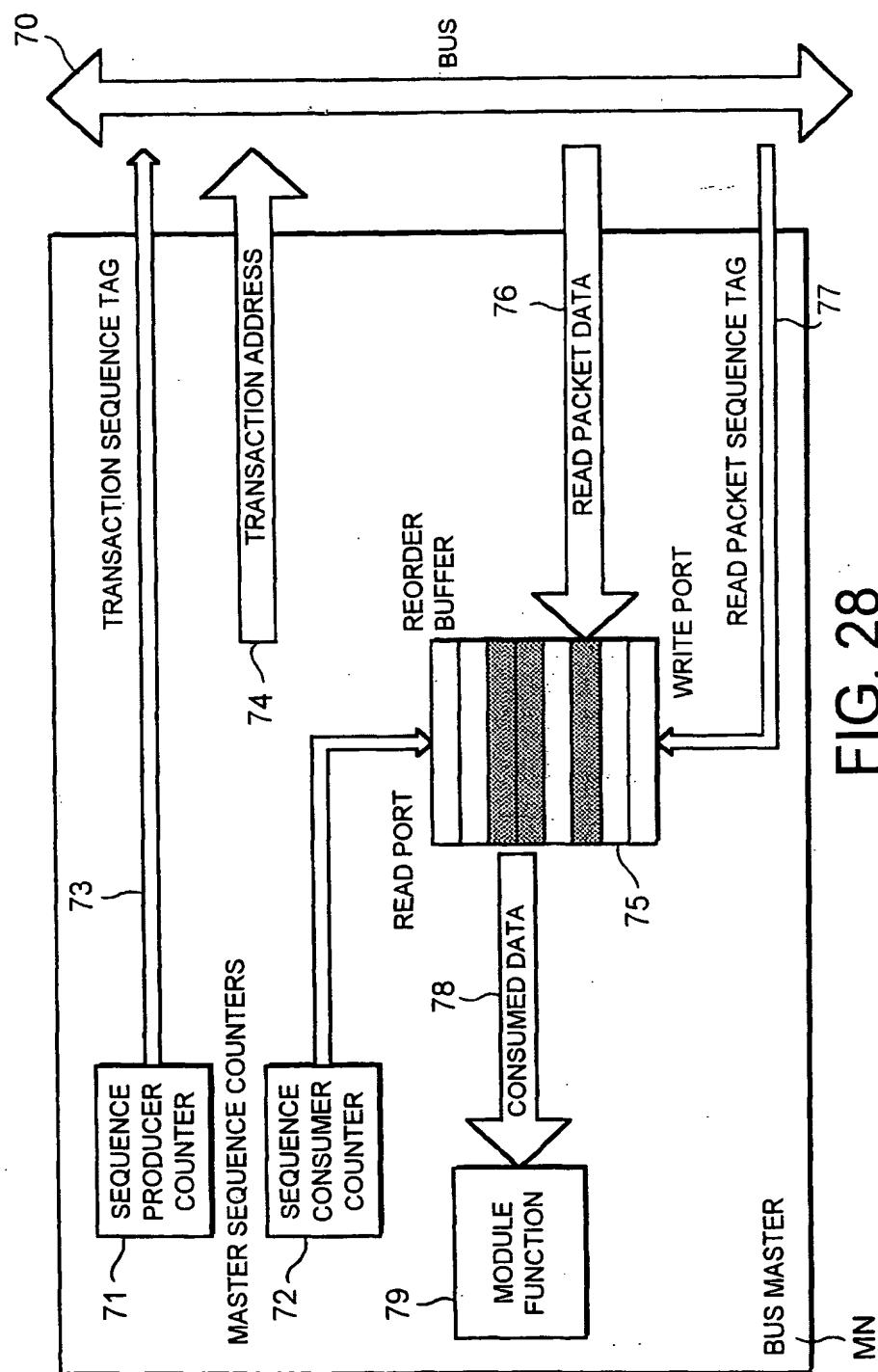


FIG. 28

Application Title: Apparatus For Use In A Computer System

Application Number: Unassigned (Div'l of 09/787,353)

Application Filing Date: April 20, 2004

Inventor: Richard Carl PHELPS et al.

Sheet 14 of 16

14 / 16

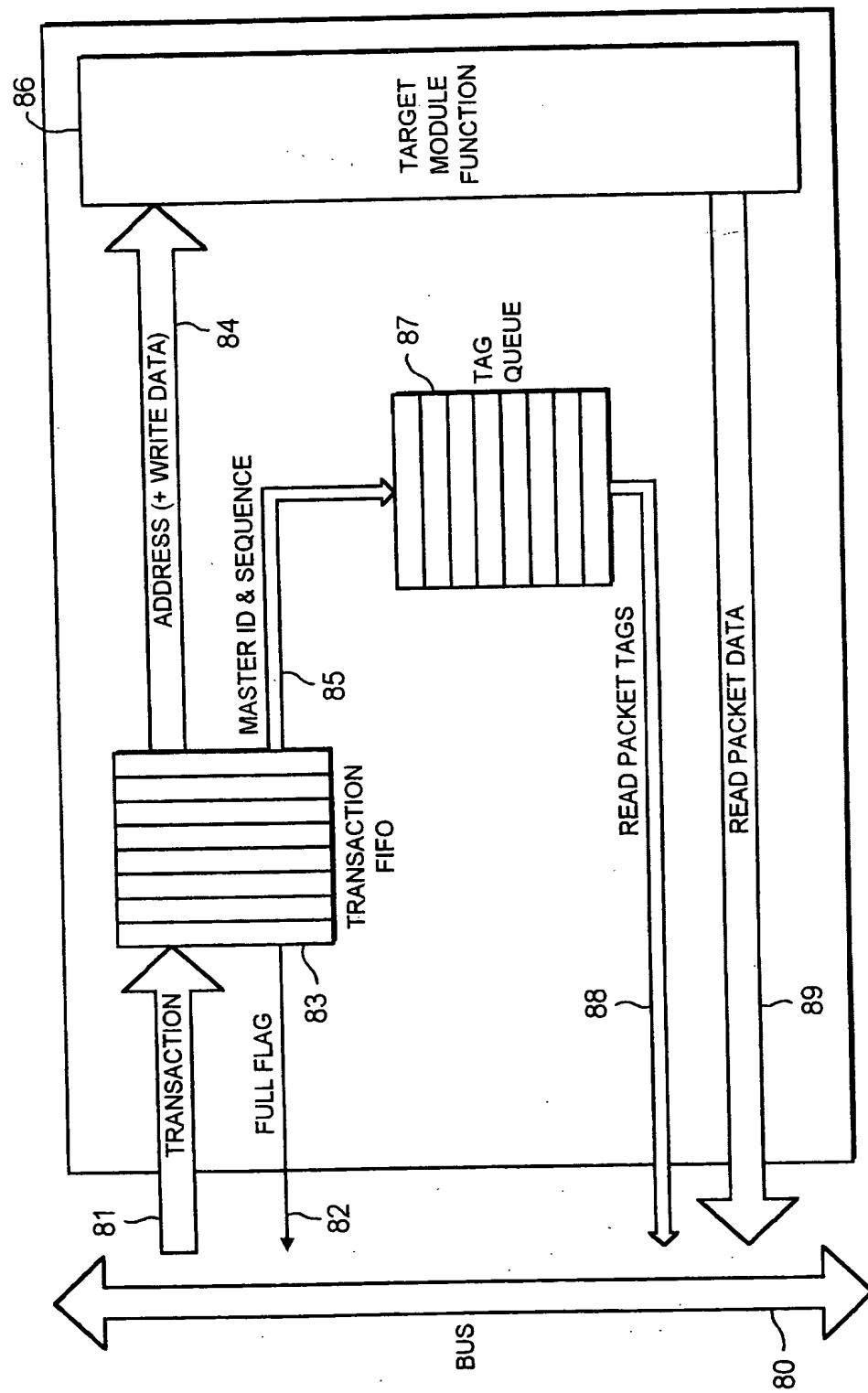


FIG. 29

Application Title: Apparatus For Use In A Computer System
Application Number: Unassigned (Div'l of 09/787,353)
Application Filing Date: April 20, 2004
Inventor: Richard Carl PHELPS et al.

Sheet 15 of 16

15 / 16

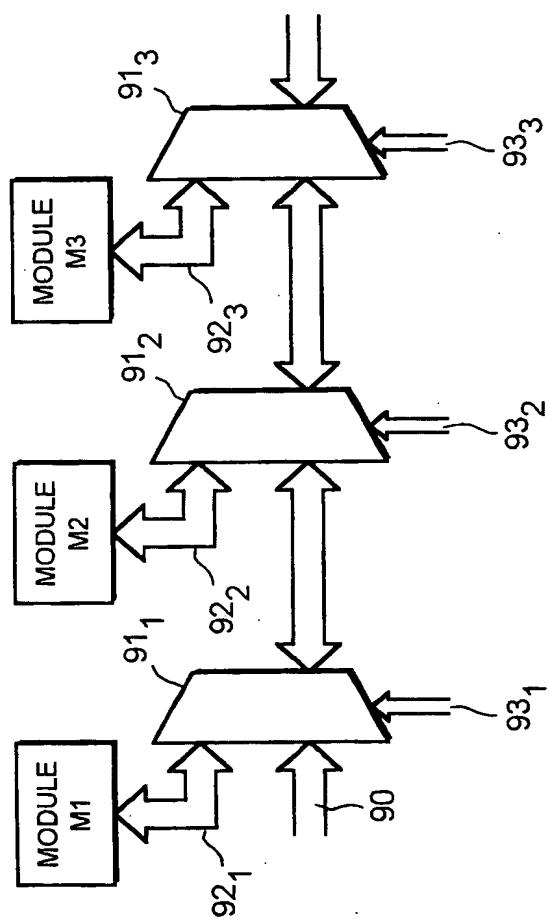


FIG. 30

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Application Filing Date: April 20, 2004

Inventor: Richard Carl PHELPS et al.

Sheet 16 of 16

16 / 16

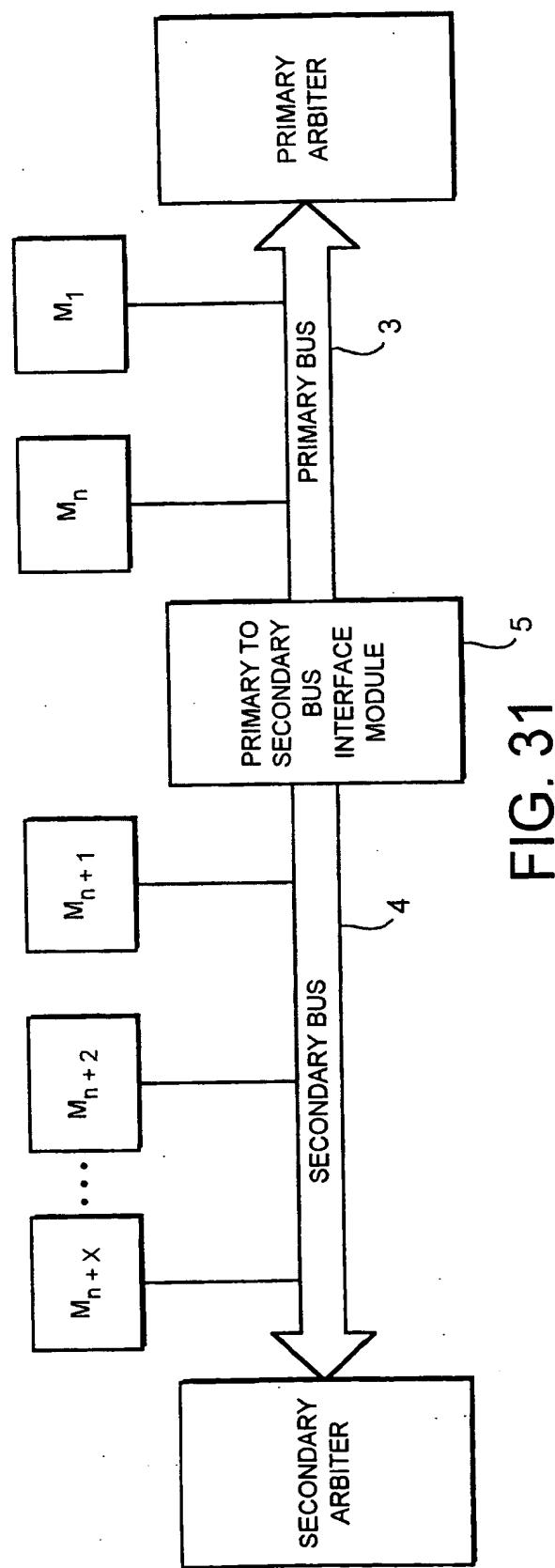


FIG. 31